	Type	L #	Hits	Search Text
1	BRS	L1	68	(measur$6 detect$5 sens$5 transducer determin$6) near3 (twist distor$6 deform$6) near3 (rotat$6) near3 (shaft)
2	BRS	L2	0	1 and ("pre-program$6")
3	BRS	L3	27	1 and (processor computer CPU ECU controller microprocessor microcontroller)
4	BRS	L4	20	3 and (torque)
5	BRS	L5	4	4 and (program$5)
6	BRS	L6	4	5 and (time)

	DBs	Time Stamp	Comments	Error Definition	Errors
1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:31			0
2	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:32			0
3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:33			0
4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:33			0
5	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:39			0
6	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/02/09 11:39			0